

# **Notice of References Cited**

Application/Control No.

09/127,584

Applicant(s)/Patent Under  
Reexamination  
RAYNAUD ET AL.

Examiner

Douglas W. Sergeant

Art Unit

2763

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## **U.S. PATENT DOCUMENTS**

*		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
<input checked="" type="checkbox"/>	A	US005870585A	Feb. 1999	Stapleton	703	15	<input type="checkbox"/>	<input type="checkbox"/>
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## **FOREIGN PATENT DOCUMENTS**

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## **NON-PATENT DOCUMENTS**

*		DOCUMENT (Including Author, Title Date, Source, and Pertinent Pages)					DOCUMENT SOURCE **	
							APS	OTHER
<input checked="" type="checkbox"/>	U	Chen et al., "A Source-Level Dynamic Analysis Methodology and Tool for High-Level Synthesis", Proceedings of the Tenth International Symposium on System Synthesis, 1997, pp. 134 - 140, September 1997.					<input type="checkbox"/>	<input type="checkbox"/>
<input checked="" type="checkbox"/>	V	Kucukcakar et al., "Matisse: An Architectural Design Tool for Commodity IC's", IEEE Design & Test of Computers, Vol. 15, Issue 2, pp. 22 - 33, June 1998.					<input type="checkbox"/>	<input type="checkbox"/>
<input checked="" type="checkbox"/>	W	Koch et al. "Debugging of Behavioral VHDL Specifications by Source Level Emulation", Proceedings of the European Design Automation Conference, pp. 256 - 261, September 1995.					<input type="checkbox"/>	<input type="checkbox"/>
<input checked="" type="checkbox"/>	X	Fang et al., "A Real-Time RTL Engineering-Change Method Supporting On-Line Debugging for Logic-Emulation Applications", Proceedings of the 34th Design Automation Conference, pp. 101-106, June 1997.					<input type="checkbox"/>	<input type="checkbox"/>

\*A copy of this reference is not being furnished with this Office action. (See Manual of Patent Examining Procedure, Section 707.05(a).)

\*\*APS encompasses any electronic search i.e. text, image, and Commercial Databases.

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PTO-892 (Rev. 03-98)

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			APS	OTHER
<input type="checkbox"/>	U	Howe, H., "Pre- and Postsynthesis Mismatches", IEEE International Conf. on Verilog HDL 1997, pp. 24-31, April 1997.	<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	V	Postula et al., "A Comparison of High Level Synthesis and Register Transfer Design Techniques for Custom Computing Machines", Proc. of the 31st Hawaii Inter. Conf. on System Sciences, Vol. 7, pp. 207-214, January 1998.	<input type="checkbox"/>	<input type="checkbox"/>
<input type="checkbox"/>	W	Orailoglu, A., "Microarchitectural Synthesis for Rapid BIST Testing", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 16, Issue 6, pp. 573-586, June 1997.	<input type="checkbox"/>	<input type="checkbox"/>
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